

Course Syllabus

Welcome to Digital System Design. This course introduces the subject of combinational and sequential logic circuits and shows you how to design logic circuits using classical methods as well as hardware description language for modern computer aided design (CAD) tools. As such, even though it will provide only moderate depth on a wide variety of fundamental topics, it will retain full technical rigor throughout.

Topics: number systems; Boolean algebra; analysis, design, and minimization of combinational logic circuits; analysis and design of synchronous and asynchronous finite state machines; and introduction to VHDL and behavioral modeling of combinational and sequential circuits.

ECE332: This lab course should be taken concurrently with ECE331. It emphasizes the lecture material of ECE331 through the use of logic synthesis and simulation software, breadboarding and testing digital circuits and exploring the fundamental electrical characteristics of logic gates.

Instructor:	Nathalia Peixoto	ST2-211	npeixoto@gmu.edu
Teaching Assistants:	Chethan Ananth	Tuesday and Wednesday laboratories(1:30-4:20 pm)	cananth@gmu.edu
	Marcello Brito	Grading and Wednesday laboratory (4:30-7:10pm)	parora@gmu.edu
	Shashi Karanam	Recitations (Fridays)	skaranam@gmu.edu

Lectures:		5:55 – 7:10pm, Tuesdays and Thursdays	Robinson Hall, B201
Recitations:	Section 301	1:30–2:20 pm, Friday	E 134
	Section 302	2:30-3:20 pm, Friday	E 134
	Section 303	3:30-4:20pm, Friday	E 134

Textbook: Fundamentals of Digital Logic with VHDL Design with CD-ROM by Stephen Brown, Zvonko Vranesic, McGraw-Hill Science/Engineering/Math; 2 edition (July 15, 2004), ISBN-10: 0072499389, ISBN-13: 978-0072499384

Office Hours:

Nathalia: Thursdays 3-5pm.

Chethan: Monday 1:30-3:30pm (ST1, 2B), Thursday 12:30-2:30pm (ST1, 2B)

Marcello: Monday 4-6pm (ST1, 2A), Thursday 1-3pm (ST1, 2A)

Shashi: Monday 2-4pm (ST1,2A); Monday 7:30-9:30pm (ST2, 203)

Recommended Background:

- [ECE 280](#) Electric Circuit Analysis
- [ECE 332](#) Digital Electronics and Logic Design Lab (can be taken concurrently)

Attendance

Attendance at recitation is required and will be recorded throughout the semester by the recitation instructor. Attendance is not taken at lecture but class participation will be noted by the instructor.

Translation: class participation will be recognized. If you excel in the exams and homeworks, your class participation is not necessary. However, if you make an effort to participate in class, and came to all recitations, you will earn the 5% (and up to 5% of extra credit).

You are responsible for all material presented in class and in the textbook.

Homework:

Homework will be assigned on a weekly basis (12 assignments total). Homework is due on Tuesday and is to be handed in on paper at the beginning of class. If you are not coming to class that day, leave the homework by Nathalia's door, before class. Homework is very helpful in preparation for exams and is required to achieve an 'A' in this class. Homework must be handed in on time to receive credit. **No late submission** is possible.

Exams:

There will be two exams during the course. Exams will be **closed book**.

A single (two-sided) blank note card (3" x 5") will be provided by the instructor on which you can write down **your own notes**. You are not allowed to use more than one card or to attach anything to this card. Your notes have to be hand written.

There will be NO make-up exams. (See Nathalia for an exception.) Students who are more than 15 minutes late for an exam may not be admitted and will be assigned a grade of zero for the exam.

- **Midterm Exam: October 16th (Tuesday).**
- **Final Exam: December 13th (Thursday).** *Obs. This is a 3h exam!!!!*

Grading:

The final grade is based on a weighted sum of your performance in exams, homeworks, recitations and class participation:

	Total
Midterm Exam	35%
Final Exam	40%
12 Homeworks	20%
Class participation / Recitation	5%
	100%

Honor Code:

All rules of the GMU Honor Code system will be in effect. You must review the rules and be familiar with them.

You are encouraged to discuss homework problems with other students and/or obtain the assistance of the recitation instructor. Nevertheless, please write down your solutions which represent your understanding of the material. Duplicating someone else's homework solutions, hardware/software designs, diagrams, source code, and exam notes is considered cheating. If you use material from other sources such as but not limited to the web, books, journals, data sheets, etc. you must reference the source. Honor code violations will be followed up with full force.

Classroom Etiquette

Cellphones have to be turned off during class or at least put into silent mode. If you have an emergency need to answer a call please quietly leave the room BEFORE answering the call.

Lectures may not be recorded without express written permission from the instructor.

Students with Disabilities

If you need special assistance, please inform the instructor soon so that we can work something out.

The Course Syllabus is Subject to Change