

# Fall 2007

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday	Labs/Reading
<b>August 27</b>	28 Lecture 1 Introduction		30 Lecture 2 Jump Start	31	<b>September 1</b>	<b>2</b>	Intro 1, 2.1-2.4, 2.9-2.10
Labor Day 3	4 Lecture 3 Boolean Alg. HW1 Due	5	6 Lecture 4 K-Maps	7	8	<b>9</b>	Equipment Intro 2.5-2.8 4.1-4.5
10	11 Lecture 5 K-Map/Design HW2 Due	12	13 Lecture 6 Number Systems	14	15	<b>16</b>	ActiveHDL Intro 5.1
17	18 Lecture 7 Comp Arith HW3 Due	19	20 Lecture 8 Codes and Design	21	22	<b>23</b>	Experiment 1 5.2-5.3, 5.8
24	25 Lecture 9 Adder in VHDL HW4 Due	26	27 Lecture 10 Multi-bit Adder	28	29	<b>30</b>	New Experi- ment 2 5.4-5.7
<b>October 1</b>	2 Lecture 11 Mux/Decoder HW5 Due	3	4 Lecture 12 Encoder	5	6	<b>7</b>	Experiment 4 6.1-6.7
Columbus Day 8	9 No class!	10	11 Lecture 13 Review HW6 Due	12	13	<b>14</b>	Experiment 8 Exam Prep
15	16 <b>Midterm</b>	17	18 Lecture 14 Testbench 1	19	20	<b>21</b>	ECE332 Midterm Chapter 3
22	23 Lecture 15 Testbench 2 HW6 Due	24	25 Lecture 16 Hazard	26	27	<b>28</b>	Exp. 6
29	30 Lecture 17 TTL HW7 Due	31	<b>November 1</b> Lecture 18 CMOS	2	3	<b>4</b>	Exp 5
5	6 Lecture 19 Delay HW8 Due	7	8 Lecture 20 D-FF	9	10	<b>11</b>	Exp 7 Chapter 7
12	13 Lecture 21 HW9 Due	14	15 Lecture 22 State Mach.	16	17	<b>18</b>	

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19	20 Lecture 23 State Mach. HW10 Due	21 Thanksgiving	22 Thanksgiving	23 Thanksgiving	24 Thanksgiving	25	Chapter 8
26	27 Lecture 24  HW11 Due	28	29 Lecture 25	30	December 1	2	Chapter 9
3	4 Lecture 26 Asynch HW12 Due	5	6 Lecture 27 Review	7	8	9	
10	11 No class!	12	13 <b>Final Exam</b>	14	15	16	
17	18 Grades will be posted!	19	20	21	22	23	

The Course Schedule is Subject to Change!!!